

CLAIMS:

1. A record carrier (1) for storing information according to a standardized format, the record carrier comprising address information (4,5,6,8) comprising
address data bits (4,5,6) indicating a position on the record carrier, the address data bits being arranged according to the standardized format, and
5 error-protection parity bits (8) for detecting errors in the address data, which deviate from the error-protection parity bits according to the standardized format.

2. A record carrier according to claim 1, wherein the error-protection parity bits (8) are being calculated using a check polynomial (P) that deviates from the check
10 polynomial according to the standardized format.

3. A record carrier according to claim 2, wherein the standardized format is the CD-R format, and the check polynomial (P) used is

$$P(X) = X^{14} + X^{12} + X^{11} + X^{10} + X^4 + X^3 + X^2 + 1$$

4. A record carrier according to claim 1, 2 or 3, wherein the standardized format is the CD-R format, and not all the error-protection parity bits (9) are inverted.

5. A record carrier according to claim 4, wherein the first ten error-protection parity bits are inverted and the last four error-protection parity bits are non-inverted.

6. A record carrier according to claim 1, wherein the address information (4,5,6,8) is recorded on the record carrier by wobbling a pregroove (2).

7. A record carrier according to claim 1, wherein the address information (4,5,6,8) are recorded on the record carrier as pre-pits.

8. A device (9) for storing information on the record carrier (1) according to anyone of the claims 1 to 7, comprising reading means (10) for reading the address data bits

(4,5,6) and the error-protection parity bits (8) present on the record carrier, error-detection means (12) for detecting errors in the address information and writing means (10) for storing information on the record carrier, wherein the error-detection means (12) are adapted for detecting the errors in the address data bits (4,5,6) using the error-protection parity bits (8) that deviate from the error-protection parity bits according to the standardized format.

9. A device according to claim 8, wherein the error-detection means (12) are adapted for detecting the errors in the address information using a check polynomial that deviates from the check polynomial according to the standardized format.

10. A device according to claim 9, wherein the standardized format is the CD-R format, and the check polynomial (P) used is

$$P(X) = X^{14} + X^{12} + X^{11} + X^{10} + X^4 + X^3 + X^2 + 1$$

11. A device according to claim 8, 9 or 10, wherein the standardized format is the CD-R format, and not all the error-protection parity bits (9) are inverted.

12. A device according to claim 11, wherein the first ten error-protection parity bits are inverted and the last four error-detection data bits are non-inverted.